

Call for Participation



25th IFIP/IEEE International Conference on Very Large Scale Integration

VLSI-SoC 2017

October 23 – 25, 2017
Yas Viceroy, Yas Island
Abu Dhabi, United Arab Emirates
<http://www.vlsi-soc.com>

Updates

- Advance registration DEADLINE: Wed, September 20th, 2017
- Online registration DEADLINE: Fri, October 20th, 2017
- Hotel reservation DEADLINE: Fri, October 6th, 2017
- Advance program is live: <http://www.vlsi-soc.com/program>
- Keynotes: <http://www.vlsi-soc.com/keynotes>
- Conference registration is open: <http://www.vlsi-soc.com/registration>
- Hotel reservation is open: <http://www.vlsi-soc.com/accommodation>
- We are grateful to all our sponsors: <http://www.vlsi-soc.com/sponsors>

Program Preview

This year's VLSI-SoC will feature 4 confirmed keynotes and 14 sessions of oral presentations, including 3 special sessions and 1 embedded tutorial. These 14 sessions will be running in two parallel tracks. There is also one poster session and a PhD Forum, highlighting the work of our budding researchers.

In line with the IoT theme of this year's conference, the program includes two special events: IoT@UAE, featuring presentations on IoT-related research in the UAE, and IoT demos contributed by graduate students from UAE universities.

Please consult the [advance technical program](#) for more details about the conference activities and events.

Timeline

- Advance registration: September 20th, 2017
- Online registration: October 20th, 2017
- Conference Hotel Reservation: October 6th, 2017
- TPC Meeting: October 22nd, 2017
- Conference: October 23rd - 25th, 2017
- Co-located Workshop: October 26th, 2017

VLSI-SoC 2017

This is the 25th in a series of international conferences sponsored by the International Federation of Information Processing Technical Committee 10 Working Group 5 (IFIP TC W10.5), the IEEE Council on Electronic Design Automation (IEEE CEDA) and the IEEE Circuits and Systems Society (IEEE CASS) which explore the state-of-the-art in the areas of Very Large Scale Integration (VLSI) and System-on-Chip (SoC) design.

Previous conferences have taken place in Edinburgh, Trondheim, Tokyo, Vancouver, Munich, Grenoble, Gramado, Lisbon, Montpellier, Darmstadt, Perth, Nice, Atlanta, Rhodes, Florianópolis, Madrid, Hong Kong, Santa Cruz, Istanbul, Playa del Carmen, Daejeon and Tallinn.

The purpose of VLSI-SoC is to provide a forum to exchange ideas and showcase academic as well as industrial research in architectures, circuits, devices, design automation, verification, test, and security, within digital, analog, and mixed-signal systems.

VLSI-SoC 2017 is held on Yas Island in the city of Abu Dhabi, capital of the United Arab Emirates, home of the Etihad Formula One Grand Prix, and a major cultural and commercial center on the Arabian Gulf.

General Chairs

- Ibrahim (Abe) M. Elfadel, Masdar Institute at Khalifa University of Science and Technology, UAE, ielfadel@masdar.ac.ae
- H. Fatih Ugurdag, Ozyegin University, Turkey, fatih.ugurdag@ozyegin.edu.tr

Program Chairs

- Mihalis Maniatakos, NYU Abu Dhabi, UAE, michail.maniatakos@nyu.edu
- Matteo Sonza Reorda, Politecnico di Torino, Italy, matteo.sonzareorda@polito.it

Local Arrangement Chair

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