2022 Phil Kaufman Awardee Announced

Dr. Giovanni De Micheli, Professor of Electrical Engineering and Computer Science in the Institute of Electrical Engineering (IEL) at the School of Engineering, as well as at the School of Computer and Communication Sciences, of the École Polytechnique Fédérale de Lausanne (EPFL) in Lausanne, Switzerland, has been honored with the 2022 Phil Kaufman Award for Distinguished Contributions to Electronic System Design. The award is presented annually by the Electronic System Design Alliance (ESD Alliance), a SEMI Technology Community, and the Institute of Electrical and Electronics Engineers (IEEE) Council on Electronic Design Automation (CEDA).

Dr. De Micheli was recognized for his extensive contributions to electronic design automation (EDA). His EDA tools and methodologies research have helped drive significant advances in the academic field of design automation and made a lasting impact on the industry through their incorporation into commercial EDA solutions. Notably, he developed multiple technologies while inspiring his students, university researchers, and engineers in the industry. His work has expanded the fields of high-level synthesis, logic synthesis, and Network-on-Chip (NoC) for more than 30 years. Read More

2022 Ernest S. Kuh Early Career Award

IEEE CEDA congratulates Bei Yu on being selected as the recipient of this year’s Kuh Early Career Award for contributions to machine learning in physical design and design for manufacturability.

The IEEE CEDA Ernest S. Kuh Early Career Award honors an individual who has made innovative and substantial technical contributions to the area of Electronic Design Automation in the early stages of his or her career. Read More

ICCAD 2022 Keynotes and Program

IEEE/ACM International Conference on Computer-Aided Design (ICCAD) is the premier forum to explore the new challenges, present leading-edge innovative solutions, and identify emerging technologies in the electronic design automation research areas. The 41st edition of the conference is to be held in San Diego, California, USA from 30 October - 3 November 2022!

Keynote Speakers

Jason Cong (UCLA) on Democratizing IC Design and Customized Computing

Farinaz Koushanfar (University of California San Diego) on Automated Cryptographically-Secure Private Computing: From Logic and Mixed-Protocol Optimization to Centralized and Federated ML Customization

Shankar Krishnamoorthy (Silicon Realization Group) on Atoms to Silicon to Systems Hyper-Convergence: The Way Forward in the Angstrom Era

The preliminary program can be viewed on the ICCAD website.

2022 Embedded Systems Week Program

Embedded Systems Week (ESWEEK) is the premier event covering all aspects of hardware and software design for intelligent and connected computing systems. By bringing together three leading conferences (CASES, CODES+ISSS, EMSOFT), two symposia (MEMOCODE, NOCS), and several workshops, tutorials, and education classes.

ESWEEK 2022 will be a week-long and truly hybrid event, with in-person and online events in Shanghai and Phoenix as well as online-only events. The program will run from 7-14 October 2022 around the clock. Check out the schedule on the ESWEEK website.

Virtual Distinguished Lecturer Webinar on 31 October

Join us on Monday, 31 October at 10-11AM ET for a virtual DL talk provided by Anupam Chattopadhyay.
on “Electronic Design Automation for Emerging Technologies (Tutorial).” Learn more and register.

The Virtual Distinguished Lecturer Program (VDLP) allows us to continue to serve the CEDA participants and the electronic design automation community the opportunity to hear from our respected Distinguished Lecturers.

Registration is free for all webinars. If you are unable to attend the "live" virtual events, the presentations will be available on our Presentation Library and the CEDA YouTube channel after the event.

**CAD for Assurance Webinar 7 October**

The CAD for Assurance webinar series is an educational initiative that provides CEDA members with access to relevant CAD tools to use. This includes information on major CAD tools the research community has developed over the past decade, including open-source license-free or ready-for-licensing tools, associated metrics, relevant publications, and video demos.

The 2022 series will conclude on 7 October with presentations from Nils Albatrus on DANA: Universal Data Flow Analysis for Gate-Level Netlist Reverse Engineering and Jonathan Cruz on Hardware Trojan Benchmarks: From Chips to PCB.

Registration is free for the webinars, however, is required in order to attend. Additional information can be found on the CEDA CAD for Assurance website.

**CADathlon Brasil 2022 Highlights**

The CADathlon Brasil 2022 - 2nd Brazilian Programming Contest for Design Automation of Integrated Circuits took place on 2 August 2022 in Niteroi, Rio de Janeiro State, Brazil. It was a co-located event of the 42nd Annual Congress of Brazilian Computer Society. It was organized by the Federal University of Santa Catarina and Fluminense Federal University.

1st Place – Team Turma da Monica from the University of Brasili, and was formed by Enzo Yoshio Niho and Eduardo Quirino de Oliveira

2nd Place – Team Rabisco UFSC from the Federal University of Santa Catarina and was formed by Arthur João Lourenço and Bernardo Borges Sandoval.

The top two teams were awarded money prizes offered by Synopsys. Read more.

**Call for Papers: Embedded Systems Letters Special Issue on Latest Advances in Embedded Systems Research in Latin America**

In Latin America, a large number of researchers work in the field of Embedded Systems. Their valuable results got published across their universities and geographical region, though not abroad. Recently, that started to change with events like the Argentine Conference on Embedded Systems (CASE) and their partnership with international publications. In our experience, advances in Embedded Systems made in the region are an exciting contribution to the worldwide scientific community.

The deadline to submit is 15 November 2022. Read more on the CEDA website.