



## BANGALORE INSTITUTE OF TECHNOLOGY

# IEEE BIT CAS PRESENTS CAD INFRASTRUCTURE FOR VLSI DESIGN

### Key Workshop Features:

**Hands-On Learning with  
industry-standard CAD  
tools.**

**Expert Facilitator  
providing real-world  
insights.**

**Networking Opportunities  
with industry experts.**

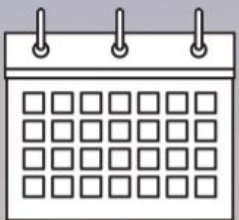
**Certificate of Completion**



**Alok Das**  
Director  
Labs and Lectures  
Semiconductor Design  
Partner

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**1st & 2nd FEB**  
**10 AM**

**VENUE: VLSI LAB**

**PROF. GAHAN A V**  
**CO-ORDINATOR**

**DR. JALAJA S**  
**IEEE BIT**  
**COUNSELLOR**

**DR. J PRAKASH**  
**VICE PRINCIPAL**

**DR. ASWATH M U**  
**PRINCIPAL**