



"TWO DAYS WORKSHOP ON CHIP DESIGN USING CADENCE TOOLS"

Organized by

Department of ECE, P.A. College of Engineering

In association with

Ignited minds of Electronics club

supported by

IEEE Council of Electronics Design Automation



TUE-WED

27-28 FEB 2024



TIME

9.30 AM TO 4:30 PM



EC computer lab



SPEAKER

MR.P.RAJENDRA

VLSI Application Engineer,
Entuple Technologies Pvt. Ltd.

Staff Coordinator,
Dr.Mohammed Zakir B

Limited seats

REGISTER NOW

Student Coordinator,
Shaheem Rahaman
shaheemrahaman123@gmail.com
+91 9632105389

