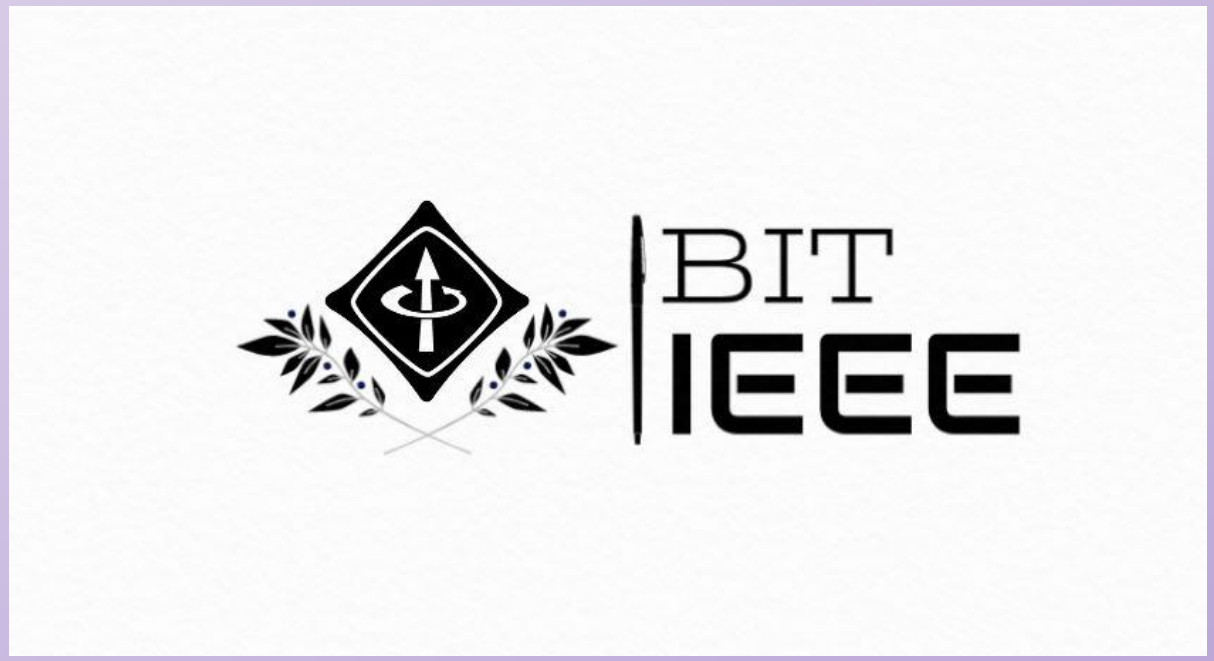
IEEE BIT CEDA STUDENT CHAPTER



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ACTIVITIES EXECUTED

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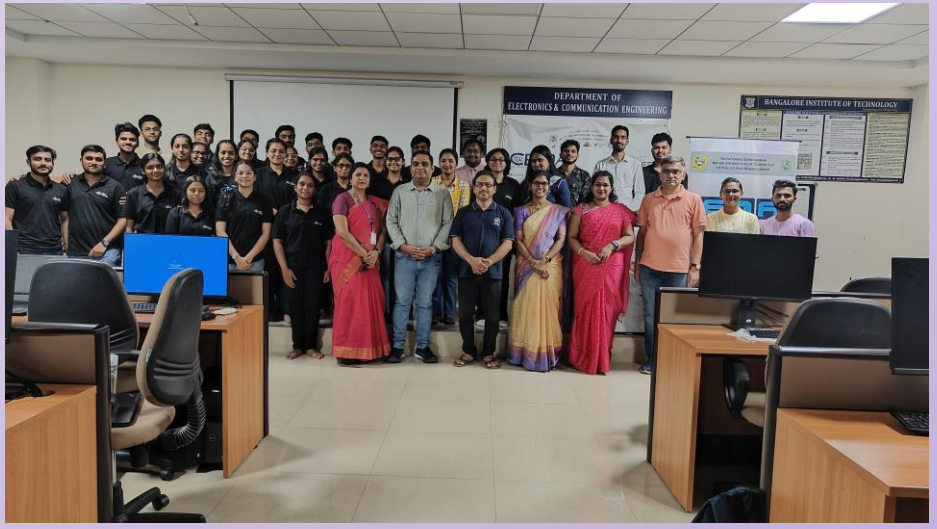

The IEEE BIT CEDA Student Chapter inaugural ceremony featured a technical talk by Mr. Sajal Mittal, a technologist at Western Digital, on the revolutionary 3D NAND memory technology. The event aimed to enlighten attendees on the latest advancements in storage technology and celebrate the beginning of the new student chapter

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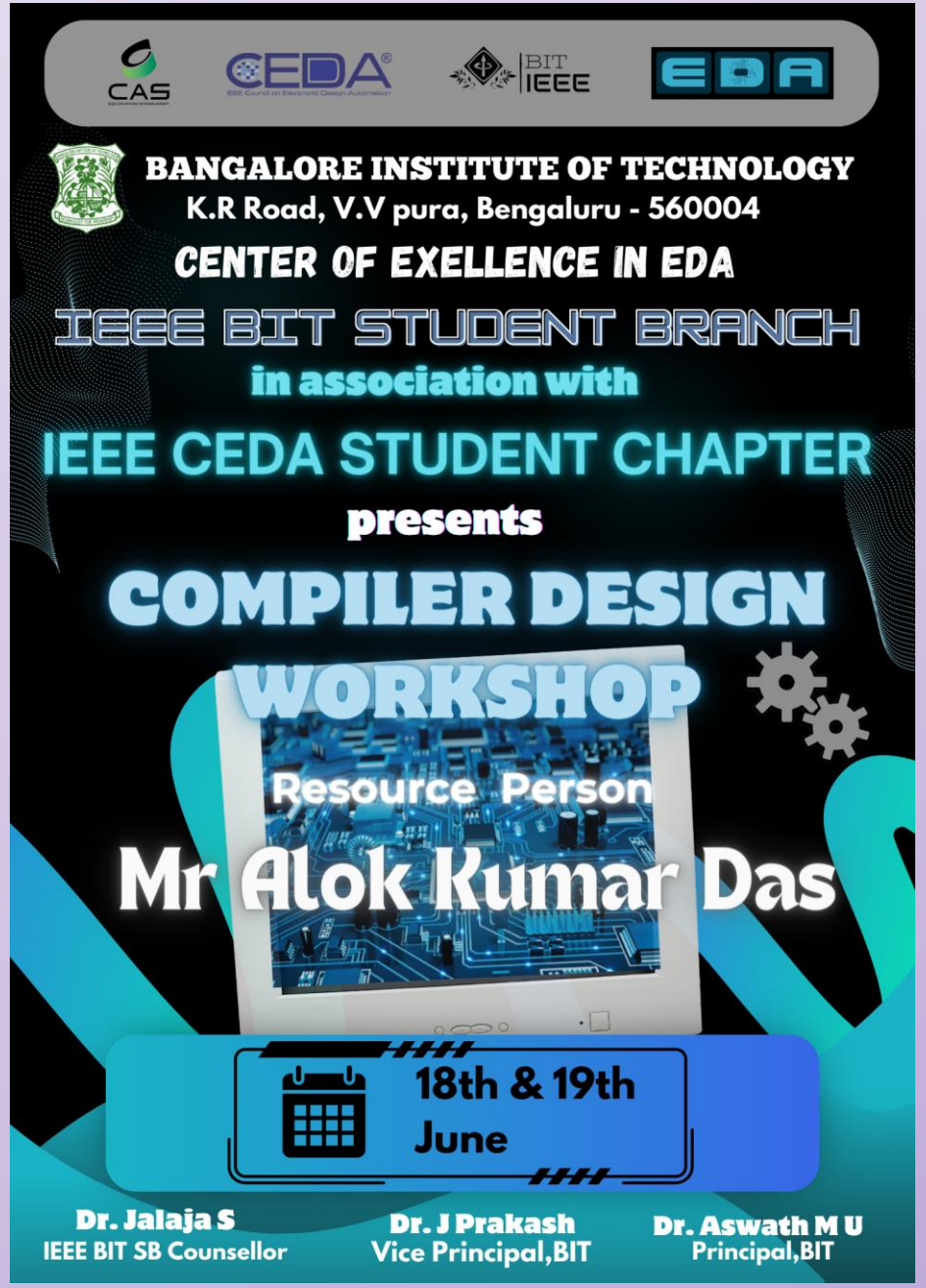

The workshop, organized by EDA Society of IEEE BIT SB, aimed to provide a comprehensive understanding of Verilog compiler design using Lex and Yacc tools. A selective test on Verilog preceded the workshop, with 10 participants chosen to attend the two-day session led by Mr. Alok Das.

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ACTIVITIES PLANNED

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MONTH	ACTIVITY
JULY	SYSTEM VERILOG WORKSHOP
AUGUST	STEM ACTIVITY
SEPTEMBER	INDUSTRIAL VISIT
OCTOBER	PANEL DISCUSSION
NOVEMBER	TECH TALK ON OPEN SOURCE EDA TOOLS

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THANK YOU

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