

Newsletter











December 2024

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2025 IEEE CEDA Fellows

The IEEE Council on Electronic Design Automation has successfully elevated five individuals to IEEE Fellow! Congratulations to

- Umit Ogras
- Patrick Schaumont
- Peilin Song
- Sheldon Tan
- Wenjian Yu

Learn More



IEEE CEDA Chapter of the Year

The IEEE Council on Electronic Design Automation Chapter of the Year will be awarded for the first time in 2025 and seeks to recognize a CEDA Chapter for significant activity and service to its members and the EDA community. The winning Chapter will receive \$1,000 and a certificate to be presented at an IEEE CEDA-sponsored conference.

Application Deadline: 28 February 2025

Learn More



ASP-DAC 2025: Registration Open & CEDA 20th Anniversary Panel

Registration is now open for the **2025 Asia and South Pacific Design Automation Conference (ASP-DAC)**, the largest conference in Asia and South-Pacific regions on Electronic Design Automation (EDA) area for VLSI and systems.

Conference Dates: 20-23 January 2025 Location: Tokyo, Japan

Join us for the CEDA 20th Anniversary Panel at ASP-DAC 2025. Don't miss this exciting opportunity to hear from leading experts as they reflect on two decades of the Council of Electronic Design Automation (CEDA) and discuss the future of design automation in the Asia and South Pacific region.

Panel Date: 22 January 2025

Learn More



DATE 2025: Registration Open

Registration is now open for the 28th Design, Automation, and Test in Europe Conference (DATE 2025). DATE is the main European event bringing together designers and design automation users, researchers, and vendors, as well as specialists in hardware and software design, and test and manufacturing of electronic circuits and systems. DATE puts a strong emphasis on both technology and systems, covering ICs/SoCs, emerging technologies, embedded systems, and embedded software.

Date: 31 March-2 April 2025 Location: Lyon, France

Register Now



IEEE Design&Test

IEEE D&T is soliciting submissions for its Special Issue on "Emerging Challenges with 3D NAND Flash Storage."

This special issue is dedicated to the design and test of 3D NAND flash storage in computing systems. It aims to cover diverse aspects of technology, foundry, design, EDA, test to in-field deployment, and integration into IoT, CPS, edge, and cloud solutions.

Topics of interest include:

- Computing in/near 3D NAND Flash storage
- Data sanitization issues in 3D NAND Flash storage
- 3D NAND Flash storage for harsh environments
- Sustainable practices for 3D NAND Flash storage
- 3D NAND Flash storage lifecycle management
- Latency enhancements for 3D NAND Flash storage
- Fault-tolerant 3D NAND Flash Storage Submission Guidelines
- Energy-efficient 3D NAND Flash storage
- Resource management for 3D NAND Flash storage
- Security vulnerabilities of 3D NAND Flash storage
- Supply chain security of 3D NAND Flash storage
- 3D NAND Flash for High-Performance Computing
- 3D NAND Flash for Embedded and IoT Computing

Submission Deadline: 1 February 2025



IEEE CEDA System Validation and Debug Technology Council Events

Dr. Sankaran Menon presented a tutorial as a part of the IEEE Test Technology Technical Council at the IEEE International Test Conference held in San Diego on 4 November 2024.

Learn More



CEDA Madras Section Chapter Events

The CEDA Madras Section Chapter held several technical talk events in November. A technical talk on "The Art of Variable Frequency Clock: Evolution and Applications" was held on 22 November 2024, and a technical talk on "Automation of GaN-based Power Amplifier" was held on 20 November 2024.

Learn More



CEDA Dayananda Sagar College of Engineering Student Branch Chapter Events

The CEDA Dayananda Sagar University-Bangalore Student Branch Chapter held several events in August and November 2024. Knowledge insights on "NAND Flash Memory: Innovating To Meet World's Storage Needs" was held on 23 November 2024. A technical talk on "PCB Design: Best practices for High-performance and Reliability" was held on 15 November 2024. The inauguration event of IEEE CEDA Dayananda Sagar College of Engineering Student Branch Chapter was held on 31 August 2024.

Learn More

Conferences



Asia and South Pacific Design Automation Conference (ASP-DAC 2025)

- 1 20-23 January 2025
- P Tokyo, Japan



Design, Automation, and Test in Europe Conference (DATE 2025)

- 1 31 March-2 April 2025
- Lyon, France



Design Automation Conference (DAC 2025)

- 1 22-26 June 2025
- San Francisco, CA, USA



Embedded Systems Week (ESWEEK 2025)

- i 28 September-3 October 2025
- P Taipei, Taiwan



International Conference on Computer-Aided Design (ICCAD 2025)

- 1 2025 TBA
- Munich, Germany



Contact IEEE CEDA VP of Conferences Tsung-Yi Ho for sponsorship opportunities at vp.conferences@ieee-ceda.org.

Publications

IEEE CEDA financially sponsors and co-sponsors several publications and publishes its *Currents* newsletter to inform the EDA community on industry news and research results.

Visit the individual publication pages for more information on publication scope, call for papers, and more.



IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems

- ViA: A Novel Vision-Transformer Accelerator Based on FPGA
- TrueNorth: Design and Tool Flow of a 65 mW 1 Million Neuron Programmable Neurosynaptic Chip
- WAVING Goodbye to Manual Waveform Analysis in HDL Design With WAL



IEEE Design&Test

- High-Bandwidth Memory (HBM) Test Challenges and Solution
- Ethical Design of Computers: From Semiconductors to IoT and Artificial Intelligence
- Testability and Dependability of AI Hardware: Survey, Trends, Challenges, and Perspectives



IEEE Embedded Systems Letters

- An Al-Based Ventilation KPI Using Embedded loT Devices
- On-Device Personalization for Human Activity Recognition on STM32
- Design of Leading Zero Counters on FPGAs



Contact IEEE CEDA VP of Publications Jörg Henkel at **henkel@kit.edu** or the journal's respective EiC for questions regarding CEDA-sponsored publications.

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