VIT CHENNAI
Vellore Institute of Technology
(Deemed to be University under section 3 of UGC Act, 1956)

CEDA
IEEE Council on Electronic Design Automation
MADRAS SECTION

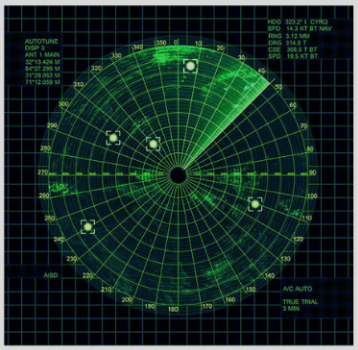
ICSD
INTEGRATED CIRCUITS AND SYSTEM DESIGN CLUB

OFFICE OF STUDENT WELFARE
VIT CHENNAI

THE ART OF VARIABLE FREQUENCY CLOCK

Evolution and Applications

SCAN HERE TO REGISTER



Dr. Alak Majumder

- Senior Member Grade IEEE
- Director of Integrated Circuit and System Lab,National Institute of Technology Arunachal Pradesh

FRIDAY
22 NOV 2024

TIME
4:30 PM-5:30 PM

LOCATION
AB3 - 312

Event Description

Join us for a guest lecture on Variable Frequency Clocks (VFCs), a cutting-edge technology that optimizes power and performance in digital systems. Learn how VFCs dynamically adjust clock speeds based on workload demands, reducing energy consumption during low activity and boosting performance when needed. Ideal for mobile, embedded, and high-performance devices, VFCs are key to creating more efficient, eco-friendly electronics. The lecture will explore the challenges of implementing VFCs, including circuit complexity and stability issues, and highlight recent advancements in adaptive clock management that are shaping the future of sustainable technology

STUDENT COORDINATOR

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