



DAYANANDA SAGAR COLLEGE OF ENGINEERING

Department of Electronics and Communication Engineering

IEEE - DSCE CEDA Chapter

brings to you



KNOWLEDGE INSIGHTS ON



NAND Flash Memory: Innovating To Meet World's Storage Needs

presented by

Dr. Sudipta Dutta



PRINCIPAL ENGINEER, WESTERN DIGITAL
BENGALURU, KARNATAKA, INDIA



EVENT DETAILS

Date: November 23rd, 2024
Venue: PC Sagar Auditorium
Time: 9:30 am - 11:30 am

CEDA-DSCE Faculty Advisor: Dr. P Vimala

Faculty Co-ordinators: Dr. Thenmozhi | Prof. Chaitra A

Office-Bearers: Indrani A | Sathvik Rao | Sumukh V | Dhaavani | Kavya P | Manohar M | Saadyant PR

Dr. B G Prasad
Principal, DSCE

Dr. Suma V
Vice-Principal & IEEE-DSCE
SB Counselor, DSCE

Dr. Shobha K R
Prof & HOD, ECE
Secretary, IEEE Bangalore Section

Dr. Kiran Gupta
IEEE-DSCE
SB Advisor, DSCE