## Newsletter

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**February 2025**

- **Upcoming Awards Deadlines**
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- **MEMOCODE 2025**

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&raquo; **CONFERENCES**     &raquo; **PUBLICATIONS**

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## Upcoming Awards Deadlines

The Council on Electronic Design Automation has several scholarship and awards deadlines approaching soon! Apply or submit a nomination today!

- **IEEE CEDA Chapter of the Year Award**
  28 February 2025
- **A. Richard Newton Technical Impact Award**
  28 February 2025
- **IEEE TCAD Donald O. Pederson Best Paper Award**
  28 February 2025
- **IEEE CEDA Ernest S. Kuh Early Career Award**
  15 April 2025
- **Phil Kaufman Award/Phil Kaufman Hall of Fame**
  30 May 2025

**Learn More**



*Congratulations!*

Partha Pratim Pande

IEEE Design&Test
2025-2026 Editor-in-Chief

# IEEE Design&Test Editor-in-Chief

Partha Pratim Pande has accepted another term as Editor-in-Chief of IEEE Design&Test for the period January 2025 - December 2026. Congratulations, Partha!

**Learn More about IEEE D&T**



# IEEE Embedded Systems Letters

IEEE Embedded Systems Letters (ESL) now has a one-month submission-to-first decision time. This means that authors can expect to receive a decision on your manuscript within one month of submission, significantly expediting the publication timeline and ensuring swift dissemination of your research findings. IEEE ESL is the premier venue for publishing early research results in an easily digestible format. Our concise four-page paper format enables authors to succinctly present their most promising research findings, making IEEE ESL the ideal platform for sharing groundbreaking discoveries with the embedded systems research community. We do this while maintaining the highest standards of quality and rigor in our peer review process.

**Learn More about IEEE ESL**

## DATE 2025: Registration Open

Registration is now open for the 28th Design, Automation, and Test in Europe Conference (DATE 2025). DATE is the main European event bringing together designers and design automation users, researchers, and vendors, as well as specialists in hardware and software design, and test and manufacturing of electronic circuits and systems. DATE puts a strong emphasis on both technology and systems, covering ICs/SoCs, emerging technologies, embedded systems, and embedded software.

**Date: 31 March-2 April 2025**
**Location: Lyon, France**

**Register Now**

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## IEEE Design&Test

IEEE D&T is soliciting submissions for its Special Issue on " LLM-aided Hardware Design, Verification and Test."

This special issue is dedicated to LLM-based hardware design, verification and test.

Topics of interest include (but not limited to):
LLM-based digital circuit design
LLM-based analog/mixed-signal/RF circuit design
LLM-based design verification
LLM-based assertion generation
LLM-based synthesis
LLM-based layout optimization
LLM-based hardware security
Applications of LLM for hardware testing
LLM-based silicon lifecycle management

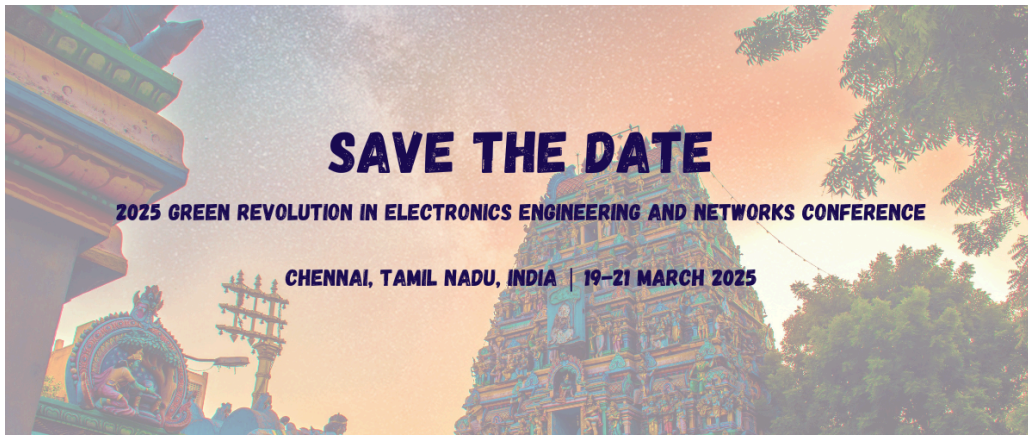**Submission Deadline: 1 April 2025**

**Learn More**



# IEEE Journal on Exploratory Computational Devices and Circuits

This special issue of the IEEE Journal on Exploratory Computational Devices and Circuits (JXCDC) aims to call for the recent research advances that address both the challenges and opportunities for information processing and storage with ferroelectric devices and circuits. Papers on co-design and optimization across multiple domains including materials, devices, circuits and architecture/systems are encouraged.

**Submission deadline: 15 May 2025**

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## 2025 Green Revolution in Electronics Engineering and Networks Conference

The Green Revolution in Electronics Engineering and Networks Conference (GREENCON 2025) is slated for March 19–21, 2025, at Vellore Institute of Technology, Chennai, Tamil Nadu, India. The School of Electronics Engineering at VIT Chennai is excited to host this conference, providing a platform to explore cutting-edge research in electronics, communication, and networks, with an emphasis on sustainable and eco-friendly advancements. It aims to facilitate the exchange of innovative ideas and research findings among professionals and researchers. This conference will enrich the participants' knowledge, fostering further research and innovation.

**Conference Dates: 19-21 March 2025**

**Learn More**



## 2025 International Forum on MPSoC for Software-Defined Hardware

The International Forum on MPSoC for Software-Defined Hardware (MPSoC

2025) will feature an outstanding program with 60 world-class R&D speakers to cover the key topics for decentralized architectures targeting AI and embedded computing. The experts discuss fundamental and strategic issues to master advanced technologies for SW vs. HW design and semiconductor manufacturing to build energy-efficient many-core and many IP architectures.

**Conference Dates: 15-20 June 2025**

**Learn More**



## 2025 International Conference on LLM-Aided Design

The 2025 IEEE International Conference on LLM-Aided Design (ICLAD 2025) will focus on how to use LLM (Large Language Model) as a methodology to help design circuits, software, and computing systems with improved quality, productivity, robustness, and cost. It is the first of its kind international conference in the community that will focus on discussing results that leverage the significant advancement and innovation captured by the generative AI and LLM technology to offer new methods and solutions for design automation, software development, among other fields, targeting various existing and emerging applications.

**Conference Dates: 26-27 June 2025**

**Learn More**

## International Symposium on Formal Methods and Models for System Design

Over the last decade, the boundaries between computer system components, such as hardware, software, firmware, middleware, and applications, have blurred. This evolution in system design and development practices led in 2014 to a change in the title and scope of the MEMOCODE conference from its original focus on hardware/software co-design to its new focus on formal methods and models for developing computer systems and their components. MEMOCODE's objective is to emphasize the importance of models and methodologies in correct system design and development, and to bring together researchers and industry practitioners interested in all aspects of computer system development, to exchange ideas, research results and lessons learned.

**Conference Dates: 2-3 October 2025**

**Learn More**

## Conferences

Design, Automation, and Test in Europe Conference (**DATE 2025**)
🗓 31 March-2 April 2025
📍 Lyon, France

Design Automation Conference (**DAC 2025**)
🗓 22-26 June 2025

📍 San Francisco, CA, USA

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Embedded Systems Week (**ESWEEK 2025**)
🗓 28 September-3 October 2025
📍 Taipei, Taiwan

International Conference on Computer-Aided Design (**ICCAD 2025**)
🗓 26-30 October 2025
📍 Munich, Germany

Asia and South Pacific Design Automation Conference (**ASP-DAC**)
🗓 TBD 2026
📍 Hong Kong

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Contact IEEE CEDA VP of Conferences Tsung-Yi Ho for sponsorship opportunities at **vp.conferences@ieee-ceda.org**.

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# Publications

IEEE CEDA financially sponsors and co-sponsors several publications and publishes its *Currents* newsletter to inform the EDA community on industry news and research results.

Visit the individual publication pages for more information on publication scope, call for papers, and more.
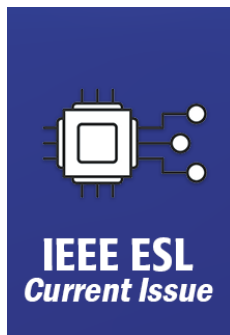
## Top Accessed Articles in January 2025

## IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems

- **ViA: A Novel Vision-Transformer Accelerator Based on FPGA**
- **TrueNorth: Design and Tool Flow of a 65 mW 1 Million Neuron Programmable Neurosynaptic Chip**
- **MNSIM 2.0: A Behavior-Level Modeling Tool for Processing-In-Memory Architectures**

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## IEEE Design&Test

- **High-Bandwidth Memory (HBM) Test Challenges and Solution**
- **A Survey of Silicon Photonics for Energy-Efficient Manycore Computing**
- **A Reinforcement Learning Framework With Region-Awareness and Shared Path Experience for Efficient Routing in Networks-on-Chip**

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## IEEE Embedded Systems Letters

- **On-Device Personalization for Human Activity Recognition on STM32**
- **An AI-Based Ventilation KPI Using Embedded IoT Devices**
- **Embedded Systems Education: Experiences With Application-Driven Pedagogy**

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Contact IEEE CEDA VP of Publications Jörg Henkel at **henkel@kit.edu** or the journal's respective EiC for questions regarding CEDA-sponsored publications.

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