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April 2025

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- MEMOCODE 2025



CONFERENCES



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ANNOUNCEMENTS



CALLBOARD



Upcoming Awards Deadlines

The Council on Electronic Design Automation has several scholarship and awards deadlines approaching soon! Apply or submit a nomination today!

- **Phil Kaufman Award/Phil Kaufman Hall of Fame**
30 May 2025

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Call for Distinguished Lecturers

Know an expert in research, teaching, and service who is also an inspiring speaker? Nominate them for the IEEE CEDA Distinguished Lecturer (DL) Program!

- ◆ Must be nominated by a CEDA participant (no self-nominations)
- ◆ If you are looking for a nominator we encourage you to contact the chair of your corresponding CEDA Local Chapter.
- ◆ The DL nominee must be a well-recognized expert in their field because of their research, teaching, and service activities, and an inspiring speaker.

Nomination Deadline: **30 April 2025**

[Submit Nomination](#)

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A banner for the MLCAD 2025 International Symposium on Machine Learning for CAD. The background is a light gray with a geometric pattern of overlapping triangles. On the left, the MLCAD logo is displayed, with 'ML' in red and 'CAD' in blue. To the right of the logo, the text 'International Symposium on Machine Learning for CAD' is written in a dark blue, sans-serif font. Below this text, a blue rounded rectangle contains the text 'Abstract Submission Deadline: 16 May 2025' in white, bold, sans-serif font.

MLCAD International Symposium on
Machine Learning for CAD

Abstract Submission Deadline:
16 May 2025

Call for Papers

The symposium focuses on Machine Learning (ML) applications to all aspects of CAD for electronic circuits, chips and systems. It is sponsored by IEEE CEDA (Council on Electronic Design Automation) and ACM SIGDA (Special Interest Group on Design Automation). MLCAD 2025 will start with the welcome reception in the evening of September 7, 2025. Papers and presentations should cover one or more aspects of applying machine learning and AI to enhance CAD of chip designs. Such aspects include, but are not limited to, algorithms, software, models, example applications,

benchmarking, and innovative solutions such as Large Language Models for CAD.

Important Deadlines

Abstract Submission: **16 May 2025**

Full Paper Submission: **23 May 2025**

[Submit Abstract](#)

[Learn More](#)

A promotional banner for the Virtual Distinguished Lecturer Program. The banner has a dark blue background with a green circuit board pattern on the left. In the center, there is a light blue rounded rectangle containing the text "Virtual Distinguished Lecturer Program" in bold white font, and a button that says "REGISTER FOR FREE TODAY" in green. To the right of the text is a small inset image showing a large audience seated in a lecture hall, facing a screen. The CEDA logo, which includes the text "IEEE Council on Electronic Design Automation", is positioned at the top center of the banner.

Virtual Distinguished Lecture

Visualization Tools

Ricardo Reis

EDA (electronic design automation) tools paved the way for the integration of billions of components in a single integrated circuit. However, many tools still rely on text interfaces. The use of visualizations tools can help not only to provide a visual output of an EDA tool but also to show how a tool is performing in terms of quality of results. A good graphic output can be useful to improve algorithms or develop new ones. The talk is illustrated with several examples, specially, some placement visualization tools are shown and it is provided a perspective how these visualization tools contributed to the improvement and development of new algorithms developed by our group.

Live Date and Time: **15 May 2025, 09:00 EDT (UTC-04:00)**

[Register](#)



ICCAD 2025 Call for Proposals

Researchers are invited to submit original work in the form of panels, special sessions, tutorials, and workshops to the 2025 International Conference on Computer-Aided Design (ICCAD 2025). Topics of interest include:

- System-Level CAD
- Synthesis, Verification, Physical Design, Analysis, Simulation, and Modeling
- CAD For Emerging Technologies, Paradigms

ICCAD follows a double-blind review policy.

Nomination Deadline: **21 April 2025**

[Learn More](#)



CALL FOR PAPERS

**IEEE Journal on Exploratory Solid-State
Computational Devices and Circuits
(JxCDC)**

IEEE Journal on Exploratory Computational Devices and Circuits

This special issue of the IEEE Journal on Exploratory Computational Devices and Circuits (JxCDC) aims to call for the recent research advances that address both the challenges and opportunities for information processing and storage with ferroelectric devices and circuits. Papers on co-design and optimization across multiple domains including materials, devices, circuits and architecture/systems are encouraged.

Submission deadline: 15 May 2025

[Learn More](#)

SAVE THE DATE

2025 International Forum on MPSoC for Software-Defined Hardware

Megeve, France | 15-20 June 2025

2025 International Forum on MPSoC for Software-Defined Hardware

The International Forum on MPSoC for Software-Defined Hardware (MPSoC 2025) will feature an outstanding program with 60 world-class R&D speakers to cover the key topics for decentralized architectures targeting AI and embedded computing. The experts discuss fundamental and strategic issues to master advanced technologies for SW vs. HW design and semiconductor manufacturing to build energy-efficient many-core and many IP architectures.

Conference Dates: 15-20 June 2025

[Learn More](#)



2025 International Conference on LLM-Aided Design

The 2025 IEEE International Conference on LLM-Aided Design (ICLAD 2025) will focus on how to use LLM (Large Language Model) as a methodology to help design circuits, software, and computing systems with improved quality, productivity, robustness, and cost. It is the first of its kind international conference in the community that will focus on discussing results that leverage the significant advancement and innovation captured by the generative AI and LLM technology to offer new methods and solutions for design automation, software development, among other

fields, targeting various existing and emerging applications.

Conference Dates: 26-27 June 2025

[Learn More](#)



International Symposium on Formal Methods and Models for System Design

Over the last decade, the boundaries between computer system components, such as hardware, software, firmware, middleware, and applications, have blurred. This evolution in system design and development practices led in 2014 to a change in the title and scope of the MEMOCODE conference from its original focus on hardware/software co-design to its new focus on formal methods and models for developing computer systems and their components. MEMOCODE's objective is to emphasize the importance of models and methodologies in correct system design and development, and to bring together researchers and industry practitioners interested in all aspects of computer system development, to exchange ideas, research results and lessons learned.

Conference Dates: 2-3 October 2025

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Conferences



Design Automation Conference (**DAC 2025**)

📅 22-26 June 2025

📍 San Francisco, CA, USA



Embedded Systems Week (**ESWEEK 2025**)

📅 28 September-3 October 2025

📍 Taipei, Taiwan



International Conference on Computer-Aided Design (**ICCAD 2025**)

📅 26-30 October 2025

📍 Munich, Germany



Asia and South Pacific Design Automation Conference (**ASP-DAC**)

📅 TBD 2026

📍 Hong Kong



Contact IEEE CEDA VP of Conferences Tsung-Yi Ho for sponsorship opportunities at vp.conferences@ieee-ceda.org.

Publications

IEEE CEDA financially sponsors and co-sponsors several publications and publishes its **Currents** newsletter to inform the EDA community on industry news and research results.

Visit the individual publication pages for more information on publication scope, call for papers, and more.

Top Accessed Articles in March 2025



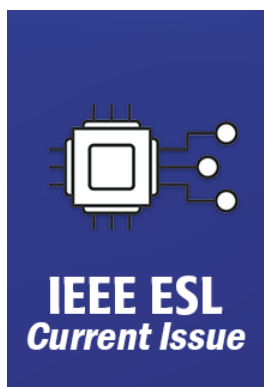
IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems

- **TrueNorth: Design and Tool Flow of a 65 mW 1 Million Neuron Programmable Neurosynaptic Chip**
- **NueroSim: A Circuit-Level Macro Model for Benchmarking Nuero-Inspired Architectures in Online Learning**
- **ViA: A Novel Vision-Transformer Accelerator Based on FPGA**



IEEE Design&Test

- **High-Bandwidth Memory (HBM) Test Challenges and Solution**
- **FlooNoC: A Multi-Tb/s Wide NoC for Heterogeneous AXI4 Traffic**
- **MAGICAL: An Open-Source Fully Automated Analog IC Layout System from Netlist to GDSII**



IEEE Embedded Systems Letters

- **On-Device Personalization for Human Activity Recognition on STM32**
- **An AI-Based Ventilation KPI Using Embedded IoT Devices**
- **An Efficient Hardware Accelerator for Block Sparse Convolutional Neural Networks on FPGA**



Contact IEEE CEDA VP of Publications Jörg Henkel at henkel@kit.edu or the journal's respective EiC for questions regarding CEDA-sponsored publications.

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