

# Call for Papers

## International Workshop on Boolean Problems (IWSBP 2026)

Bremen, Germany

The International Workshop on Boolean Problems (IWSBP) provides a forum for the scientific exchange of ideas on problems related to Boolean logic and Boolean algebra, including problems of a discrete mathematical nature. The workshop brings together researchers and engineers from different disciplines to discuss challenges, present novel solutions, and explore emerging research directions.

IWSBP is devoted to both theoretical advances and practical applications in Boolean reasoning and related areas. A key goal of the workshop is to foster collaborative research and identify new application domains.

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## Publication Model

IWSBP follows a journal-first publication model in cooperation with IEEE and IEEE Embedded Systems Letters (ESL).

Authors are invited to submit original research articles formatted as full-length research papers (maximum 4 pages) according to the IEEE ESL formatting guidelines. Detailed formatting and submission instructions are available in the IEEE ESL Author Instructions:

<https://iee-veda.org/publication/esl/esl-author-instructions>

Papers accepted through this track will be published in IEEE Embedded Systems Letters, providing a peer-reviewed and indexed journal publication.

## Presentation Requirement

All accepted papers must be presented by at least one author at IWSBP 2026, and author registration for the workshop is mandatory.

## Major Revision Submissions

Authors whose papers receive major revision decisions during the IEEE ESL review process are welcome to present their revised work at IWSBP. For more details, please visit the workshop website:

<https://www.informatik.uni-bremen.de/iwsbp/>

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# Venue

Bremen, Germany

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## Topics of Interest

Topics of interest include, but are not limited to:

- Boolean logic
  - Bitvector arithmetic
  - Verification and validation
  - Applications to real-world problems
  - Dynamic analysis
  - Reasoning problems and algorithms
  - Beyond Boolean logic (e.g., multiple-valued, quantum, reversible systems)
  - Digital VLSI Design
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## Important Dates

- Paper Submission Deadline: ~~June 16, 2026~~ June 30, 2026
  - Author Notification: ~~July 16, 2026~~ July 30, 2026
  - Camera-Ready Deadline: August 11, 2026
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## Submission

Manuscripts should be submitted through the IEEE ESL submission system:

<https://ieeesubmission.researchexchange.com/submission/dashboard?siteName=ies-ieee>

During submission, please select the option indicating that the paper should be considered for presentation at a workshop. This can be done by selecting the IWSBP option in the “Article Type” field during submission.

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## Additional Information

Further information about the workshop, program, and participation details can be found at:

<https://www.informatik.uni-bremen.de/iwsbp/>

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## Guest Editors

- Chandan Kumar Jha, University of Bremen
  - Bernhard Berger, Hamburg University of Technology
  - Farhad Merchant, University of Groningen
  - Rolf Drechsler, University of Bremen
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## Contact

For any questions or additional information, please contact:  
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