



CEDA Congratulations to Prof. M. Wolf and Prof. R. Gupta

Marilyn Wolf, Farmer Distinguished Chair in Embedded Computing Systems and Georgia Research Alliance Eminent Scholar at the Georgia Institute of Technology, has been selected to receive the IEEE Computer Society 2019 Harry H. Goode Award for “contributions to embedded, hardware-software co-design, and real-time computer vision systems.”

The Goode Award was established to recognize achievements in the information processing field which are considered either a single contribution of theory, design, or technique of outstanding significance, or the accumulation of important contributions on theory or practice over an extended time period.

Rajesh K. Gupta, professor of computer science and engineering at University of California, San Diego, has been selected to receive the IEEE Computer Society 2019 W. Wallace McDowell Award. Gupta is being recognized “for seminal contributions in design and implementation of Microelectronic Systems-on-Chip and Cyberphysical Systems.”

The McDowell Award is given to individuals for outstanding recent theoretical, design, educational, practical, or other innovative contributions in the field of computing. The award may be given for a single contribution of great merit or a series of lesser contributions that have had or are expected to have an important influence on the computer field.

Professor Wolf and Professor Gupta will receive their award at the awards dinner held on Wednesday evening, 5 June 2019 at the Hyatt Regency Coral Gables during the IEEE Computer Society Board of Governors meeting. The Goode Award consists of a bronze medal and an honorarium and an invitation to speak at the award presentation. Read more about this outstanding achievement and a list of past participants [here](#). The McDowell award consists of a bronze medal and an honorarium. Read more about this outstanding achievement and a list of past participants [here](#).

CEDA Welcomes New Distinguished Lecturers

The IEEE Council on Electronic Design Automation (CEDA) welcomes three new Lecturers continuing its successful Distinguished Lecturer Program. The new Lecturers are Professor David Pan from the University of Texas at Austin, Professor Yier Jin from the University of Florida, and Dr. Rajiv Joshi from the IBM, T. J. Watson Research Center. Their talks cover a diverse range of topics on emerging areas and technologies including Artificial Intelligence, Machine Learning for IC design, Optical Computing, and Hardware and IoT Security. You can request a DL talk for your event [here](#).

CEDA Luncheon Talk at DATE 2019

CEDA sponsored its Luncheon at DATE 2019 on Wednesday, March 27th, as a key part of the Special Day of the conference titled “Embedded meets Hyperscale and HPC”. This CEDA Luncheon talk, given by David Pellerin from Amazon, titled “Heterogeneous Computing: Embedded meets Hyperscale and HPC” addressed the rapid advances in smart connected devices, coupled with machine learning and “data lake” methods of advanced analytics in the cloud. More precisely, this CEDA Luncheon Keynote presented examples of use-cases within Amazon of public cloud computing using heterogeneous computing platforms and AI developed by Amazon (including CPUs, GPUs, FPGAs, and other emerging acceleration technologies), coupled with smart, cloud-connected devices to create next-generation intelligent products for Amazon customer. Moreover, fully linked to CEDA topics, the talk concluded with examples of how cloud-based semiconductor design is being enhanced using these same methods. The luncheon was very well received by the DATE audience. Indeed the room was completely full (with more than 150 attendees), and received compliments by many attendees in the audience. CEDA showed with this luncheon its strong support to key conferences within EDA and embedded systems, as well as building a bridge between the embedded and HPC/data center community.

CEDA President visits Morocco Chapter

As part of the promotion of CEDA activities for young professionals and students, during the period of April 2019, the Morocco CEDA Chapter has organized a number of Distinguished Lecture Program (DLP) talks during the visit of CEDA's President (Prof. David Atienza) in Marrakech, Safi and Casablanca. In this series of DLP talks, David Atienza had the opportunity to interact and explain to young researchers and engineers how they can join and participate in IEEE CEDA activities as IEEE Student Members.

In addition, Prof. Atienza gave two DLP talks about the latest trends in Internet of Things (IoT) technologies, at ENSA (École Nationale des Sciences Appliquées) in Marrakech and Safi, which are part of the main national public engineering school in Morocco. During the visits to both schools, Prof. Atienza had the opportunity to meet with the two ENSA Directors in order to strengthen the cooperation of IEEE CEDA with high-level education centers in Morocco in all aspects related to electrical engineering and computer science, EDA, and embedded systems design. Moreover, it was discussed how to organize in Morocco in the next two years a number of CEDA DLP seminars and workshops including IEEE CEDA members, as part of the growing professional education activities for engineers in the IoT space that the Morocco CEDA Chapter activities are developing as the main hub of CEDA in Africa.

IEEE Transactions on CAD Special Issue – Call for Papers

IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (TCAD) is announcing a special issue on “Hardware-Oriented Security and Trust: Threats, Countermeasures and Design Tools”, which invites top papers accepted to the 2019 Asian Hardware-Oriented Security and Trust Symposium ([AsianHOST 2019](#)) for extension and also calls for original research papers through public contributions.

The purpose of this special issue is to provide targeted readers with new advances and challenges in hardware

security research and development. Topics of interest include discoveries of emerging security threats that are encountered by the hardware design and supply chain, demonstration of the most recent hardware security attacks and mitigations, as well as new security protection techniques and design methodologies that help to thwart these threats. All paper submissions must be made through the [IEEE TCAD online paper submission](#) system. Detailed submission instructions can be found [here](#).

NOCS 2019 – Call for Contributions

The International Symposium on Networks-on-Chip (NOCS) will take place between October 17-18, 2019 in New York and will be co-located with the Embedded Systems Week. NOCS is the premier event dedicated to interdisciplinary research on on-chip, package-scale, chip-to-chip, and datacenter rack-scale communication technology, architecture, design methods, applications and systems. NOCS brings together scientists and engineers working on network-on-chip (NoC) innovations and applications from inter-related research communities, including discrete optimization and algorithms, computer architecture, networking, circuits and systems, packaging, embedded systems, and design automation. Topics of interest include but are not limited to: NoC Architecture and Implementation, Communication Analysis, Optimization, & Verification, Novel NoC Technologies, NoC for Intelligent Physical Systems, NoC at the Un-Core and System-level, Inter/Intra-Chip and Rack-Scale Network.

Electronic paper submission requires a full paper, up to 8 double-column ACM (sigconf) format pages, including figures and references.

A percentage of accepted papers will be recommended for publication in an IEEE journal after revision according to the reviewers' comments. Please find the detailed submission instructions for paper submission, special session, and demo proposals at the [submission webpage](#). The deadline for abstract submission is on May 10, 2019.

Find us online at ieee-ceda.org.

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Visit ieee-ceda.org/publication/esl-publication/author-guidelines.

IEEE Design & Test is open for submissions.
Visit ieee-ceda.org/publication/ieee-design-test-dt/paper-submission-instructions.



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