February 2020

Currents

CEDA
IEEE Council on Electronic Design Automation

ASAP 2020 – Call For Contributions

The 31st IEEE International Conference on Application-specific Systems, Architectures, and Processors (ASAP 2020) will take place in Manchester, UK between July 6-8, 2020. The history of the ASAP conference traces back to the International Workshop on Systolic Arrays, organized in 1986 in Oxford, UK. It later developed into the International Conference on Application-Specific Array Processors. With its current title, it was organized for the first time in Chicago, USA, in 1996. Since then it has alternated between Europe and North-America. The conference covers the theory and practice of application-specific systems, architectures, and processors. For the 2020 edition of ASAP, manuscripts in the following categories are especially welcomed: Sustainable Computing, Neuromorphic Computing, Technologies Beyond CMOS in addition to traditional topical research areas of the conference. Perspective authors are invited to submit original, unpublished research papers through Easychair. Submission guidelines can be found here. The submission deadlines for abstract and full paper are March 27, 2020 and April 3, 2020, respectively.

ICCAD CAD Contest 2020 – Call For Participation

The CAD Contest at ICCAD is a challenging, multi-month, research and development competition, focusing on advanced, real-world problems in the field of Electronic Design Automation (EDA). It is open to multi-person teams world-wide. Each year the organizing committee announces three challenging problems in different topic areas provided by industrial companies. Contestants can participate in one or more problems. The prizes will be awarded at an ICCAD special session dedicated to this contest. Since its inaugural year of 2012, the CAD Contest at ICCAD has been attracting more than a hundred teams per year, fostering productive industry-academia collaborations, and leading to hundreds of publications in top-tier conferences and journals. The contest undoubtedly boosts EDA research and keeps enhancing its impact.

OPRECOMP Summer of Code 2020 – Call For Proposals

The Open Transprecision Computing project OPRE-COMP supported by the European Union's H2020 – FET Proactive research and innovation programme is looking to support enthusiasts, researchers, and students from EU member and associated countries of the H2020 programme working on OPEN SOURCE PROJECTS ON TRANSPRECISION COMPUTING. Submit your project proposals until April 3rd, 2020 through the OPRECOMP summer of Code Website. Your proposals will be evaluated by a jury and ten highest-ranked projects will be supported with (up to) €6,000 per accepted project.

Accepted proposals will be announced on May 1st, 2020. Participants will be expected to attend the OPRECOMP Summer School in Cagliari (Italy) on July 7-10, 2020 to kick off their projects. The results of the projects will be presented at a final OPRECOMP workshop in February 2021. For more information on how to be part of this exciting event, please visit the OPRECOMP site.

SVDTC Recent Activities

IEEE System Validation and Debug Technology Committee (SVDTC) is a body of professionals from industry and academia encompassing different organizations across the globe. This IEEE committee grew out of the observation that system validation and debug still lacks an organized effort to address the industry challenges. To this end, the SVDTC's primary focus is on fostering innovation in the field of system validation tools/techniques, and post-implementation debug (on silicon prototypes or emulation), where the system encompasses hardware, firmware and/or system software. The aim is to tackle broad issues that are deemed beneficial to multiple partner organizations, institutions and thus have an industry-level impact on standards, specifications, problem definitions, and so on. SVDTC is also interested to help develop a system validation curriculum to establish this field as an academic discipline and therefore adequately prepare the next generation of electrical

and computer engineers. Further information about SVDTC can be found here.

SVDTC has been working on creating a white paper in the area of Scan and Array State Extraction for System on Chips. We have completed the white paper titled "**Scan and Array State Extraction for System-on-Chips**" that is available at this link.

SVDTC Debug Subcommittee created a proposal and presented a presentation titled **"Debug Sub-Committee Presentation to the EDA Industry/IEEE TTSC**' (to IEEE Test Technology Standards committee) and received approval to work on study group for standardization work on "**Scan and Array Extraction**".

- Received approval from IEEE TTSC to work on study group and we have started weekly meetings to develop the PAR (Project Authorization Request) for submission by middle of this year.

- If anyone is interested in participating in this study group on "**Scan and Array Debug**", please Email to the SVDTC contact (Sankaran.Menon@gmail.com).

SVDTC has also worked on a paper with title "Techniques for Debug of Low Power SoCs," which will be submitted for presentation at the "IEEE Microprocessor/SoC Test, Security & Verification (MTV20)" in Austin, TX (December 8-9, 2020).

Donald O. Pederson Best Paper Award – Call For Nominations

The IEEE Transactions on Computer-Aided Design Donald O. Pederson Best Paper Award is sponsored by the IEEE Council on EDA and recognizes the best paper published in the Transactions on Computer-Aided Design of Integrated Circuits and Systems publication.

The award is based on the overall quality, the originality, the level of contribution, the subject matter and the timeliness of the research. Anyone who is an author of a paper published in the Transactions on Computer-Aided Design of Integrated Circuits and Systems during the two calendar years preceding the award is eligible for nomination. (Papers published from January 1, 2018 to December 31, 2019 are eligible for the 2020 award.)

- Self-nominations are not permitted.
- Nominations must include the following
  a. Name and email of the nominator.
  b. Title of the paper, author list and the issue in which the paper appeared.
  c. Reason for nomination (where applicable, explain your qualifications to comment on the paper) -- less than 200 words.

Nominations should be sent as plain text to Xin Li by 5:00PM (EST) on **February 28, 2020**.

SMACD 2020 goes to Erfurt/Germany

The 17th International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit Design (SMACD 2020) will be held from the 20th to the 23th of July 2020 in Erfurt, Germany. The conference is a forum devoted to modeling, simulation and synthesis for Analog, Mixed-signal, RF (AMS/RF) and multi-domain (nanoelectronics, biological, MEMS, optoelectronics, etc.) integrated circuits and systems. SMACD is sponsored by IEEE, IEEE CEDA, IEEE CAS, as well as companies and academic institutions. Finally, SMACD 2020 will be co-located with PRIME 2020, bringing together well-known researchers, institutions and companies from the IC Design and EDA communities.

To enter the competition, all participants must submit a paper by March 6th, 2020 with a description of the methodology and/or tool, the design problem of their choice and the solution provided by their methodology and/or tool. It is a requirement that the first author of the submission and presenter at the conference be a MSc or a PhD student. Following previous editions of SMACD conference, a $1,000 prize, sponsored by CEDA, will be granted to the winner of the competition. For more info and to participate, visit the link.

Find us online at ieee-ceda.org.

IEEE Embedded Systems Letters is open for submissions.
Visit **ieee-ceda.org/publication/esl-publication/author-guidelines**.

IEEE Design & Test is open for submissions.
Visit **https://ieee-ceda.org/publication/ieee-design-test-dt/paper-submission-instructi**