



IEEE Council on Electronic Design Automation

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ETS 2021 – Call for Contributions

The 26th edition of the IEEE European Test Symposium (ETS) will take place between May 24-28, 2021 as a fully virtual event. ETS is Europe's premier forum dedicated to presenting and discussing scientific results, emerging ideas, hot topics, and new trends, industrial case-studies and applications, in the area of electronic-based circuits and system testing, reliability, safety, security and validation. The organizing team will strive to keep the spirit of ETS alive by organizing live sessions and many opportunities for networking and peer interaction. ETS 2021 is organized jointly by IMEC and KU Leuven, which co-sponsor the event together with the IEEE CEDA.

The program includes keynotes, scientific-paper presentations, panels, tutorials, workshops and highlights/demos from industry. Besides regular technical papers, ETS 2021 provides the opportunity of submitting scientific contributions for hot-topic papers and case-study papers (each with specific evaluation criteria). Submissions are also solicited for special sessions, panels, tutorials and workshops, and the PhD Forum. Linked to the main ETS symposium, the Test Spring School and Fringe Workshops will be organized.

ETS will produce formal proceedings of scientific papers with ISBN numbers that will be included in the IEEE *Xplore* Digital Library. All accepted technical papers submitted in one of the three categories (regular, hot topic, and case study) will be included in the formal proceedings (each following its own guidelines). Extended versions of selected papers will be invited for submission in an IEEE Journal Special Issue dedicated to ETS 2021. For more information about the conference and submissions deadlines, visit the conference [website](#).

CEDA Congratulates new IEEE Fellows

Four CEDA members have been elevated to the prestigious grade of IEEE Fellow, effective in January 2021. CEDA would like to congratulate each and every

one of them for their contributions in the following fields:

- **Yung-Hsiang Lu** for contributions to energy efficiency of computer systems
- **Dmitri Maslov** for contributions to quantum circuit synthesis and optimization, and compiling for quantum computers
- **Gang Qu** for contributions to hardware intellectual protection and security
- **Mehdi Tahoori** for contributions to resilient nanoscale integrated circuits

Phil Kaufman Award Postponed to 2021

The [Phil Kaufman Award for Distinguished Contributions to Electronic System Design](#) will be on hiatus until September 2021, the Electronic System Design Alliance (ESD Alliance) and the IEEE Council on Electronic Design Automation (CEDA) announced.

“Members of the selection committee made the difficult but understandable decision to postpone this year’s award,” remarks Bob Smith, executive director of the ESD Alliance. “The dinner ceremony is central to celebrating the recipient’s contributions to the industry and accomplishments. Hosting an in-person awards dinner in early 2021 is unlikely, which made postponing the 2020 award inevitable.”

Nominations submitted in 2020 will be treated as new nominations for 2021. As always, past nominations (2017 – 2019) will also be considered for 2021. Additional nominations will be solicited in early 2021. To learn more about the award, visit the [website](#).

CEDA Launches Online Presentation Library

Constantly pursuing new ways to accomplish its mission and considering the effects that the pandemic has in the communication among its members, CEDA recently added a new web feature to facilitate knowledge dissemination within and beyond the EDA community. The new Presentation Library will include talks from different sources such as conferences, workshops,

plenary talks, etc. that are of significant interest to the community. The content of the CEDA Presentation Library contains invited and special session talks from this year's DAC conference.

More talks and presentations will be added in the future. Check the [website](#) regularly for new content.

2020 International School on the Effects of Radiation on Embedded Systems for Space Applications (SERESSA)

SERESSA combines academic, government, and industrial communities working in the area of radiation effects on embedded systems. Radiation effects are a significant concern for space and avionics systems, as well as for critical applications operating at ground level such as automotive, high energy facilities, medical or even banking. The school is based on lectures and exercises involving real case studies using the common tools of the domain. The intended audience includes both beginning and experienced researchers, engineers, and post-graduate students wishing to enhance their knowledge base in this rapidly evolving field.

Topics covered by SERESSA include: radiation environment, spacecraft anomalies, single-event effects (SEE), total dose effects (TID), radiation effects in power systems, radiation effects in solar cells, architecture hardening in analog, and digital circuits and in memories, software hardening, effects in FPGAs, hardness assurance, rate prediction, radiation testing, laser testing and remote testing experiments.

Virtual participants are invited to present their research works and recent results during the poster sessions at the virtual program schedule. The scope of the session is to promote interaction among different groups and to stimulate possible collaborations among participants. A list of speakers and deadlines can be found [here](#).

2020 Electronic Design Automation School

We are happy to announce the first edition of the Electronic Design Automation School (EDAS 2020), an event dedicated to students and researchers in Brazil and

Latin America. EDAS 2020 will be held in virtual mode from December 7-11, 2020, and is being organized by Univ. Federal de Santa Catarina, Florianópolis, Brazil.

EDAS 2020 will offer a group of talks on key topics of Electronic Design Automation (EDA) for integrated circuits (IC) in modern and upcoming technologies, including current and future challenges faced by the industry and academia for implementing complex circuits and systems. Courses will introduce the main concepts, algorithms, computing methods and AI techniques employed in the field, so that attendees can leverage their comprehension of EDA while discovering new career opportunities and hot research topics. The courses will be given in Portuguese, with slides in English. Lecturers include professionals who stand out in the field and that have been working for international companies.

Registration will be free of charge for IEEE CASS members (both professional and student categories). A limited number of grants will be available for Brazilian students. To register and for more info, visit the [website](#).

2021 Symposium on Integrated Circuits and Systems Design (SBCCI)

The 34th edition of the Symposium on Integrated Circuits and System Design (SBCCI) is an international forum dedicated to integrated circuits and systems design, test and electronic design automation (EDA) and will be hosted on August 23 to 27, 2021. The goal of the symposium is to bring together researchers in the areas of EDA, design and test of integrated circuits and systems. The scope of the symposium includes technical sessions, tutorials and panels, as well as an exhibition and working group meetings.

The proceedings will be submitted to IEEE *Xplore* and ACM Digital Library. The best papers presented at the symposium will be invited to resubmit an extended version to be considered for publication in the IEEE Design&Test and in the Springer Nature ALOG (Analog Integrated Circuits and Signal Processing). Information about paper submission is available on the conference [webpage](#).

IEEE Embedded Systems Letters is open for submissions.
Visit ieeecd.org/publication/esl-publication/author-guidelines.

IEEE Design & Test is open for submissions.
Visit <https://ieeecd.org/publication/ieeecd-design-test-dt/paper-submission-instructions>

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