

February 2023

Currents



IEEE Council on Electronic Design Automation

Call for 2023 Award Nominations!

The IEEE Council on Electronic Design and Automation is soliciting nominations for its awards. To view the full detailed listing of each award please visit the [awards page](#) on our website.

Nominators should utilize the forms associated with each award description on the website.

Distinguished Service Award:

[Details](#) | [Nomination Form](#)

Nominations due 28 February 2023

Richard Newton Technical Impact Award in Electronic Design Automation

[Details](#) | [Nomination Form](#)

IEEE Transactions on Computer-Aided Design Donald O. Pederson Best Paper Award

[Details](#) | [Nomination Form](#)

Nominations due 1 March 2023

Fellows Program

[Details](#) | [Nomination Form](#)

Nominations due 15 April 2023

Ernest S. Kuh Early Career Award

[Details](#) | [Nomination Form](#)

Nominations due 30 June 2023

Phil Kaufman Award for Distinguished Contributions to EDA

[Details](#) | [Nomination Form](#)

Phil Kaufman Hall of Fame

[Details](#) | [Nomination Form](#)

Visit the CEEDA [website](#) to learn more about awards and to nominate a deserving colleague.

Join Us in Celebration of Giovanni De Micheli at the Phil Kaufman Dinner!

Dr. Giovanni De Micheli was the 2022 Phil Kaufman award recipient. Giovanni is being honored for his extensive contributions to electronic design automation (EDA). His research in EDA tools and methodologies has made a significant, lasting impact on the industry, including contributions to the following: High-Level Synthesis, Logic Synthesis and Minimization, Hardware Software Co-Design, Network on Chip (NoC), and Cell Library Mapping

His academic advances in EDA have had a lasting impact on the industry, which would not be where it is today without groundbreaking research.

The Phil Kaufman awards banquet will be held at The GlassHouse in San Jose, CA, USA at 6:30 PM PT. For more information or to register, click [here](#).

2023 ACM/SIGDA CEEDA ICCAD: Call for Papers

The [ACM/IEEE International Conference on Computer-Aided Design \(ICCAD\)](#) will be held in San Francisco, CA on 29 October-2 November 2023. [ICCAD](#) is the premier forum to explore the new challenges, present leading edge innovation solutions, and identify emerging technologies in the Electronic Design Automation Research area. [ICCAD](#) covers a full-range of CAD topics – from device and circuit level up through system level, as well as post-CMOs design. [ICCAD](#) has a long-standing tradition of producing cutting-edge, innovative technical program for attendees.

Papers are now being solicited for the 42nd edition of the conference. Suitable topics include, but are not limited to the following: System Level CAD, Synthesis, Verification, Physical Design, Analysis, Simulation, and Modeling, and CAD for Emerging Technologies, Paradigms.

Abstract submissions are due on Monday, 15 May 2023. Special session, tutorial, and workshop proposals and full paper submissions are due on Monday, 23 May 2023.

For a full list of topics and guidelines, visit the [ICCAD website](#).

2023 ACM/SIGDA CEDA ICCAD: Call for Papers

The [International Symposium on Low Power Electronics and Design \(ISLPED\)](#) is the premier forum for the presentation of innovative research in all aspects of low-power electronics and design, ranging from process technologies and analog/digital circuits, simulation and synthesis tools, system-level design and optimization, to system software and applications.

Papers are now being solicited for the 2023 ISPLED conference. Specific topics include, but are not limited to, the following three main tracks: Technology, Circuits, and Architecture, EDA, Systems, and Software, and Crosscutting Topics.

Technical paper submissions are due by 6 March 2023. Invited talk, panel, and embedded tutorial proposals are due 10 April 2023. For more information on topics and guidelines, please visit the [ISPLED website](#).

Register for the Next Virtual Distinguished Lecturer!

Join us on Monday, 6 March at 10-11 AM ET for a Virtual DL talk provided by Mohammad Al Farque on "Cross-Layer Security of Embedded and Cyber-Physical Systems." [Learn more](#) and [register](#).

The Virtual Distinguished Lecturer Program (VDLP) allows us to continue to serve the CEDA participants and give the electronic design automation community the opportunity to hear from our respected [Distinguished Lecturers](#).

Registration is free for all webinars. If you are unable to attend the "live" virtual events, the presentations will be available on our [Presentation Library](#) and the [CEDA YouTube channel](#) after the event.

CAD for Assurance Webinar 24 February

The [CAD for Assurance](#) webinar series is an educational initiative that provides CEDA members with access to relevant CAD tools to use. This includes information on major CAD tools the research community has developed over the past decade, including open-source license-free or ready-for-licensing tools, associated metrics, relevant publications, and video demos.

The 2023 series will begin on Friday, 24 February with a panel including David Kehlet, Murthi Ssahasivan, Serge Leef, Rainer Leupers, and Chip Hong Chang discussing

"Hardware Security 2.0: What are the New Frontiers?" [Register now](#) to attend!

Call for Papers: Design & Test Special Issue on Ethics and Computing

This special issue is dedicated to ethics in computing. It aims to cover diverse aspects of theory, practice, experiences, and open challenges related to ethical design, test, and use of computing platforms. The scope spans the entire silicon ecosystem from technology, foundry, design, EDA, test, verification and debug to in-field deployment, and integration into IoT, CPS, edge, and cloud computing platforms. The specific topics of interest include but are not limited to: Ethical design of computing chips, ethical design of cyber-physical systems, e.g., automotive, UAVs, ethical Internet of Things (IoT) design, ethics of AI and machine learning computing systems Submission Guidelines, ethical and sustainable semiconductor processes, ethical computing lifecycle management, ethical design with emerging technologies, e.g., Quantum computing, and ethics in computing education

The deadline to submit is 15 March 2023. Read more on the [CEDA website](#).



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