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DATE 2015 Conference and Exhibition

The 2015 Design, Automation and Test in Europe Conference and Exhibition took place on 9-13 March 2015 in Grenoble, France. DATE 2015 received 1,450 registrations from all over the world and ended with positive feedback from both participants and exhibitors.

This year's conference received 915 eligible paper submissions—an all-time high for DATE and a 3% increase over last year's conference. Besides the large share (45%) of submissions coming from Europe, 24% of

submissions were from North America, 28% were from Asia, and 3% were from the rest of the world.

For the 18th successive year, DATE presented an exciting technical program, comprising 78 technical sessions and 11 exhibition theatre sessions. DATE week began on Monday, 9 March, with 10 in-depth tutorials and the ACM SIGDA/EDAA (European Design Automation Association) PhD Forum.

On Tuesday, 10 March, the conference was opened by the plenary keynote speakers, Günther H. Oettinger (EU Commissioner for Digital Economy and Society) and Laurent Rémont (STMicroelectronics Group Vice President for Technology and Product Strategy, Embedded Processing Solutions Segment). Günther Oettinger talked about European Microelectronics Strategy. Laurent Rémont presented a talk on "Embracing the Digital Transformation & Enabling IoT Innovations."

On the same day, Mentor Graphics organized a successful lunchtime keynote session, and the Executive Track offered a series of business panels discussing hot topics. The accompanying exhibition comprised 20 companies, including five European projects.

The distinctive highlights of DATE 2015 were again the two special days: "Designing Electronics for the Internet of Things (IoT)" and "Designing Electronics for Medical Applications."

Keynote speaker Wolfgang Wahlster (German Research Center for Artificial Intelligence, DE) focused on "Industry 4.0: From the Internet of Things to Cyber-Physical Production Systems." Keynote speaker Antun Domic (Synopsis, USA) analyzed "The Rise of IoT and the Role of EDA." Finally, the keynote by Kristoffer Famm (GlaxoSmithKline, GB) focused on "Bioelectronic Medicines: Heralding in a New Therapeutic Approach."

The major networking event during DATE week was the DATE party, which took place in the remarkable Museum of Grenoble and included a visit of the premises for all interested delegates. The City of Grenoble was again a dedicated and generous host.

With around 300 attendees, the 10 workshops on Friday, 13 March, brought DATE week to a very successful close.

“Design Automation and Test are the necessary enablers for utilizing the latest microelectronics technologies for the implementation of a digital economy and society. DATE 2015 was the melting pot for exchanging worldwide leading research results and building bridges between technology, EDA, and applications. This makes DATE the perfect event for another successful conference in 2016,” said DATE General Chair Wolfgang Nebel.

DATE continues to attract many participants, maintaining its status as the leading EDA conference in Europe and shaping the future of the industry.

DATE 2016 will take place on 14-18 March 2016 in the beautiful city of Dresden, Germany, once again bringing together industry and research in partnership.

For further information on this conference, please visit www.date-conference.com.

IEEE Design & Test *Call for Papers and Special Issues*

IEEE Design & Test (D&T) presents original works on methods used to design and test electronic-product hardware and supportive software. The magazine focuses on current and near-future practice, and includes tutorials, how-to articles, and real-world studies.

Topics in *D&T* include IC and module design, low-power design, EDA, design and test verification, practical technology, and standards.

Please consider submitting articles of general interest with 4,500 to 5,000 words to *IEEE Design & Test* at <https://mc.manuscriptcentral.com/dandt>.

Please also consider submitting a special issue proposal for *D&T*. Information on how to do so is available at <http://ieee-ceda.org/publications/d-t/paper-submission>.

Papers in IEEE Embedded Systems Letters

The top-five accessed articles from *IEEE Embedded Systems Letters* in February 2015 were as follows:

- “[An Embedded Architecture for Energy-Efficient Stream Computing](#),” by A. Panda and K.S. Chatha
- “[Fault-Resilient Lightweight Cryptographic Block Ciphers for Secure Embedded Systems](#),” by M. Mozaffari-Kermani et al.
- “[A Security Layer for Smartphone-to-Vehicle Communication over Bluetooth](#),” by A. Dardanelli et al.
- “[Efficient Jammed Area Mapping in Wireless Sensor Networks](#),” by A. Azim et al.
- “[Function-Level Processor \(FLP\): A High Performance, Minimal Bandwidth, Low Power Architecture for Market-Oriented MPSoCs](#),” by H. Tabkhi, R. Bushey, and G. Schirner

IEEE Embedded Systems Letters is open for submissions. Please visit <https://mc.manuscriptcentral.com/les-ieee>.

Upcoming Conferences (David Atienza, david.atienza@epfl.ch)	
DAC	San Francisco, 7-11 June 2015
ESLsyn	San Francisco, 10-11 June 2015
MPSoC	Ventura Beach, CA, 13-17 July 2015
PATMOS	Bahia, Brazil, 1-4 September 2015
MEMOCODE	Austin, Texas, 21-23 September 2015
EMCAD	Austin, Texas, 27-30 September 2015
ESWeek	Amsterdam, Netherlands, 4-9 October 2015
ICCAD	Austin, Texas, 2-6 November 2015

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